



LOW NOISE DUAL OPERATIONAL AMPLIFIERS

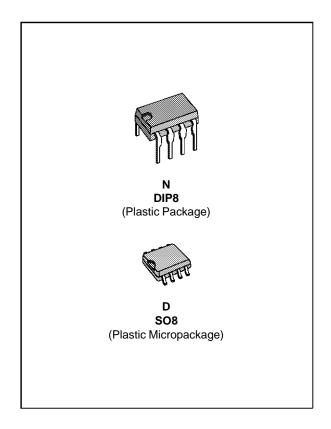
■ LOW VOLTAGE NOISE : 4.5nV/√Hz

■ HIGH GAIN BANDWIDTH PRODUCT : 15MHz

■ HIGH SLEW RATE : **7V/µs**■ LOW DISTORTION : 0.002%

■ EXCELLENT FREQUENCY STABILITY

■ ESD PROTECTION 2kV



DESCRIPTION

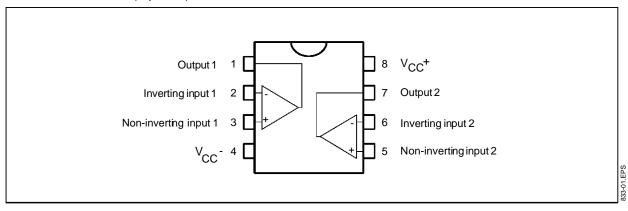
The LM833 is a monolithic dual operational amplifier dedicated to audio applications. The LM833 offers low voltage noise (4.5 nV/Hz) and high frequency performances (15 MHz gain bandwidth product, $7 \text{V/}\mu\text{s}$ slew rate).

In addition the LM833 has also a very low distortion (0.002%) and excellent phase/gain margins.

ORDER CODES

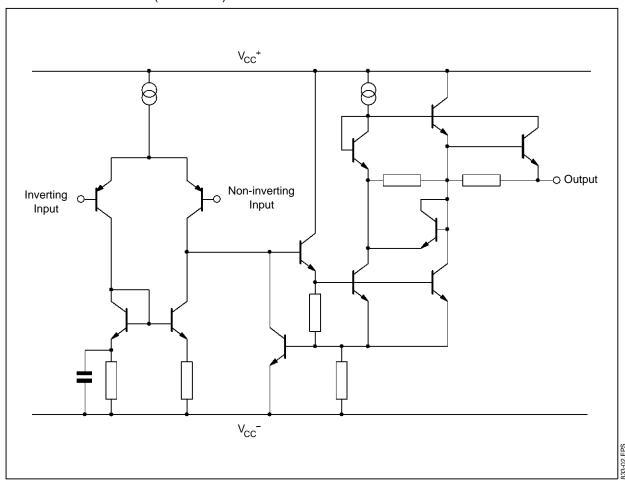
Part Number	Temperature Range	Package		
i ait itullibei	Temperature Kange	N	D	JE.
LM833	-40, +105°C	•	•	833-0

PIN CONNECTIONS (top view)



March 1994 1/5

SCHEMATIC DIAGRAM (1/2 LM833)



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
Vcc	Supply Voltage	±18 or +36	V
V _{id}	Differential Input Voltage - (note 1)	±30	V
Vi	Input Voltage - (note 1)	±15	V
	Output Short-Circuit Duration - (note 2)	Infinite	
T _{oper}	Operating Free-air Temperature Range	-40 to +105	°C
Tj	Maximum Junction Temperature	+150	°C
T _{stg}	Storage Temperature	-65 to +150	°C
P _{tot}	Maximum Power Dissipation - (note 2)	500	mW

Notes: 1. Either or both input voltages must not exceed the magnitude of V_{CC+} or V_{CC-} 2. Power dissipation must be considered to ensure maximum junction temperature (T_j) is not exceeded

OPERATING CONDITIONS

Symbol	Parameter	Value	Unit
Vcc	Supply Voltage	±2.5 to ±15	V



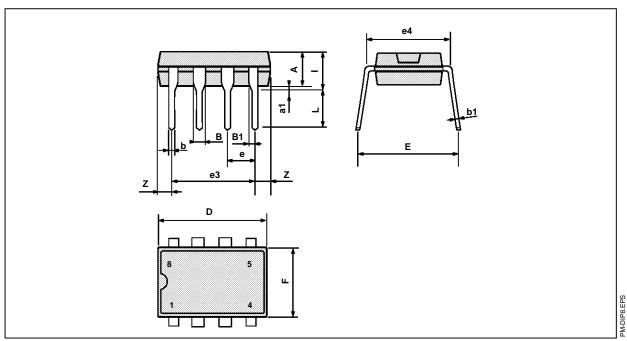
ELECTRICAL CHARACTERISTICS

 V_{CC}^+ = +15V, V_{CC}^- = -15V, T_{amb} = 25°C (unless otherwise specified)

Symbol	Parameter	Min.	Тур.	Max.	Unit
Vio	Input Offset Voltage ($R_S = 10\Omega$, $V_{ic} = 0V$, $V_0 = 0V$)		0.3	5	mV
DV _{io}	Input Offset Voltage Drift $R_S = 10\Omega$, $V_o = 0V$, $T_{min.} \le T_{amb} \le T_{max.}$		2		μV/°C
l _{io}	Input Offset Current (V _{ic} = 0V, V _O = 0V)		25	200	nA
l _{ib}	Input Bias Current (Vic = 0V, VO = 0V)		300	1000	nA
V _{icm}	Common Mode Input Voltage Range	±12	±14		V
A _{vd}	Large Signal Voltage Gain ($R_L = 2k\Omega$, $V_O = \pm 10V$)	90	100		dB
±V _{opp}	Output Voltage Swing ($V_{id}=\pm 1V$) $ \begin{array}{c} R_L=2.0k\Omega \\ R_L=2.0k\Omega \\ R_L=10k\Omega \\ R_L=10k\Omega \end{array} $	10 12	13.7 -14 13.9 -14.4	-10 -12	V
CMR	Common Mode Rejection Ratio (V _{ic} = ±12V)	80	100		dB
SVR	Supply Voltage Rejection Ratio VCC ⁺ / VCC ⁻ = +15V / -15V to +5V / -5V	80	105		dB
Icc	Supply Current (V _O = 0V, both amplifiers)		4	8	mA
SR	Slew Rate (V_i = -10V to +10V, R_L = 2k Ω , A_V = +1V)	5	7		V/μs
GBP	Gain Bandwidth Product (f = 100kHz, $R_L = 2k\Omega$, $C_L = 100pF$)	10	15		MHz
В	Unity Gain Bandwidth (Open loop)		9		MHz
Øm	Phase Margin		60		Degrees
en	Equivalent Input Noise Voltage (R _S = 100Ω, f = 1kHz)		4.5		$\frac{\text{nV}}{\sqrt{\text{Hz}}}$
İn	Equivalent Input Noise current (f = 1kHz)		0.5		pA √Hz
FPB	Full Power Bandwidth ($V_0 = 27Vpp, R_L = 2k\Omega, THD \le 1\%$)		120		kHz
THD	Total Harmonic Distortion $R_L = 2k\Omega$, $f = 20Hz$ to $20kHz$, $V_O = 3V_{rms}$, $A_V = +1$		0.002		%
V _{O1} /V _{O2}	Channel Separation (f = 20Hz to 20kHz)		120		dB

PACKAGE MECHANICAL DATA

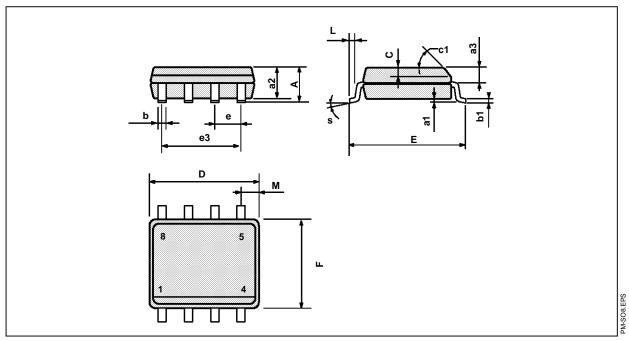
8 PINS - PLASTIC DIP



Dimensions		Millimeters				
Difficusions	Min.	Тур.	Max.	Min.	Тур.	Max.
Α		3.32			0.131	
a1	0.51			0.020		
В	1.15		1.65	0.045		0.065
b	0.356		0.55	0.014		0.022
b1	0.204		0.304	0.008		0.012
D			10.92			0.430
E	7.95		9.75	0.313		0.384
е		2.54			0.100	
e3		7.62			0.300	
e4		7.62			0.300	
F			6.6			0260
i			5.08			0.200
L	3.18		3.81	0.125		0.150
Z			1.52			0.060

PACKAGE MECHANICAL DATA

8 PINS - PLASTIC MICROPACKAGE (SO)



Dimensions		Millimeters			Inches	
Dimensions	Min.	Тур.	Max.	Min.	Тур.	Max.
А			1.75			0.069
a1	0.1		0.25	0.004		0.010
a2			1.65			0.065
a3	0.65		0.85	0.026		0.033
b	0.35		0.48	0.014		0.019
b1	0.19		0.25	0.007		0.010
С	0.25		0.5	0.010		0.020
c1			45°	(typ.)		
D	4.8		5.0	0.189		0.197
E	5.8		6.2	0.228		0.244
е		1.27			0.050	
e3		3.81			0.150	
F	3.8		4.0	0.150		0.157
L	0.4		1.27	0.016		0.050
М			0.6			0.024
S	8° (max.)					

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